

Method and system using hardware assistance for instruction tracing  
by revealing executed opcode or instruction

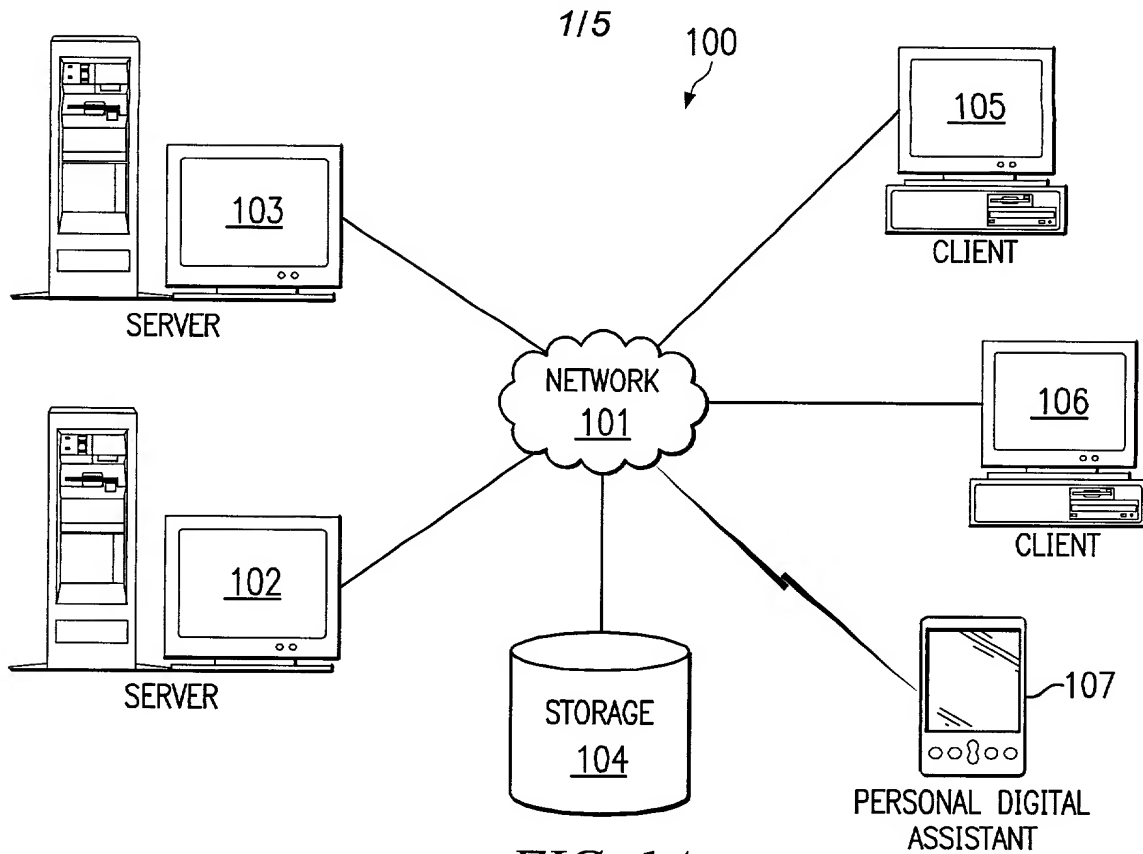


FIG. 1A  
(PRIOR ART)

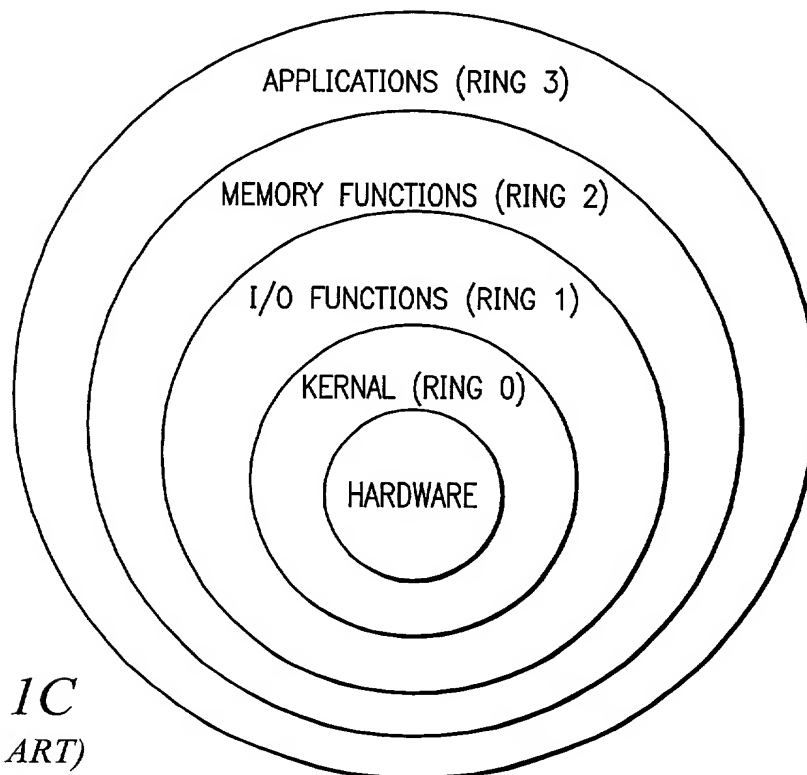
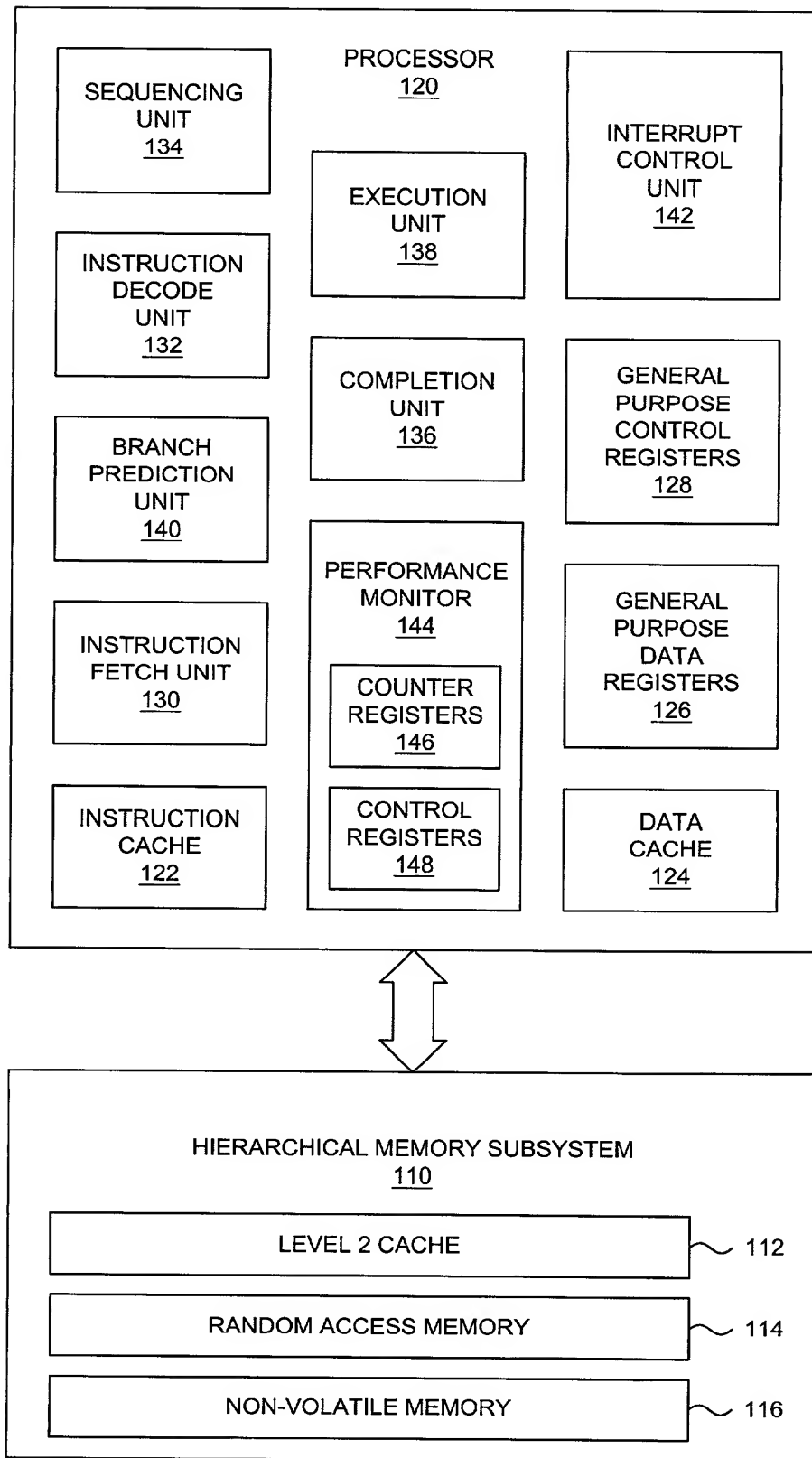


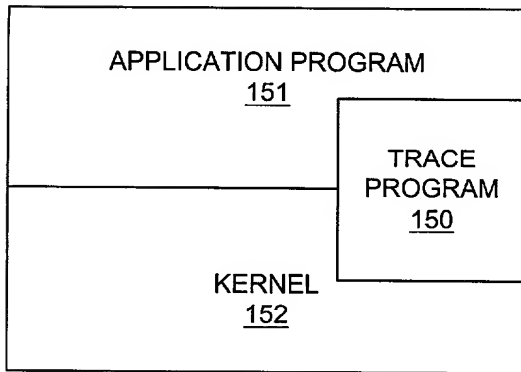
FIG. 1C  
(PRIOR ART)

2/5

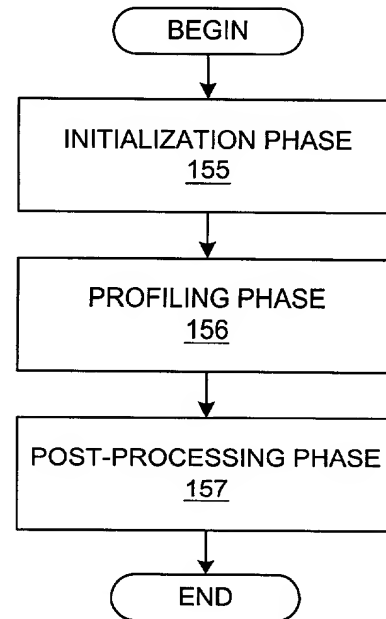


**FIG. 1B**  
(PRIOR ART)

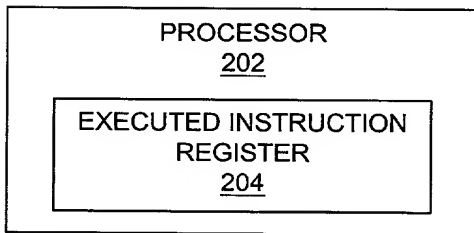
3/5



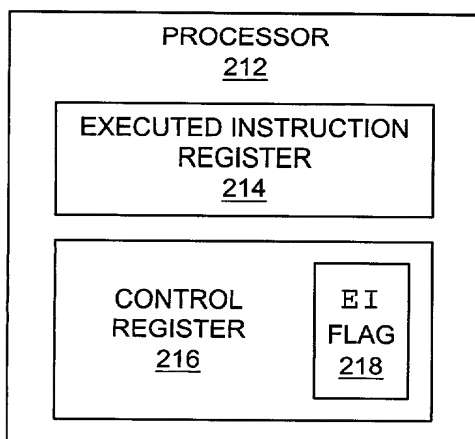
*FIG. 1D*  
(PRIOR ART)



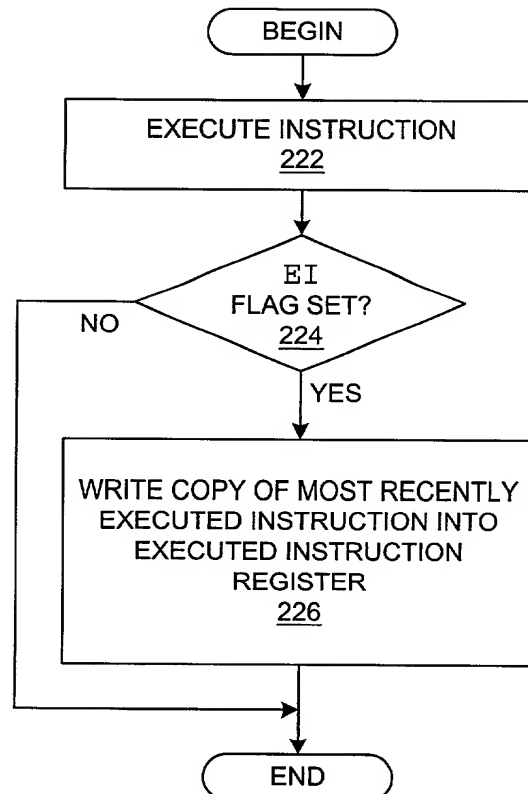
*FIG. 1E*  
(PRIOR ART)



*FIG. 2A*



*FIG. 2B*



*FIG. 2C*

Method and system using hardware assistance for instruction tracing  
by revealing executed opcode or instruction

4/5

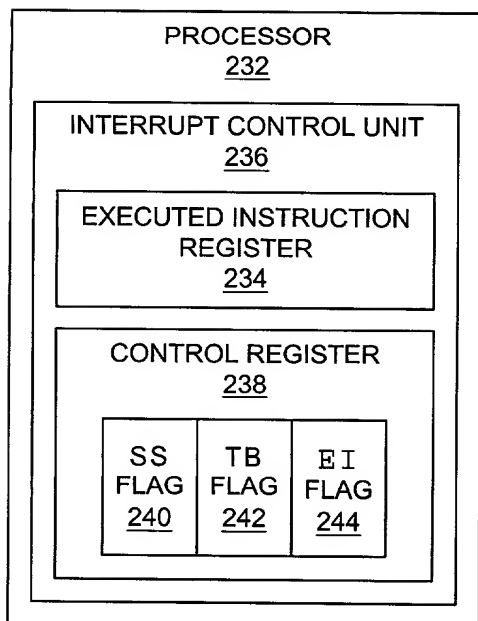


FIG. 2D

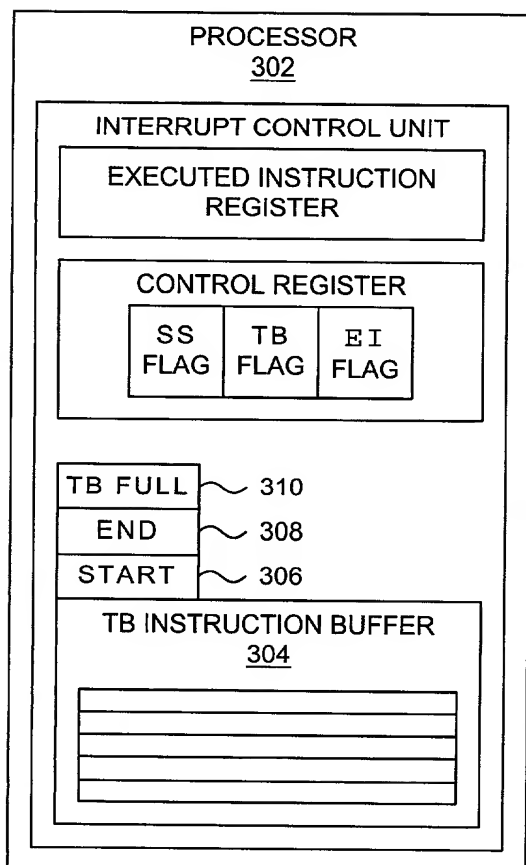


FIG. 3A

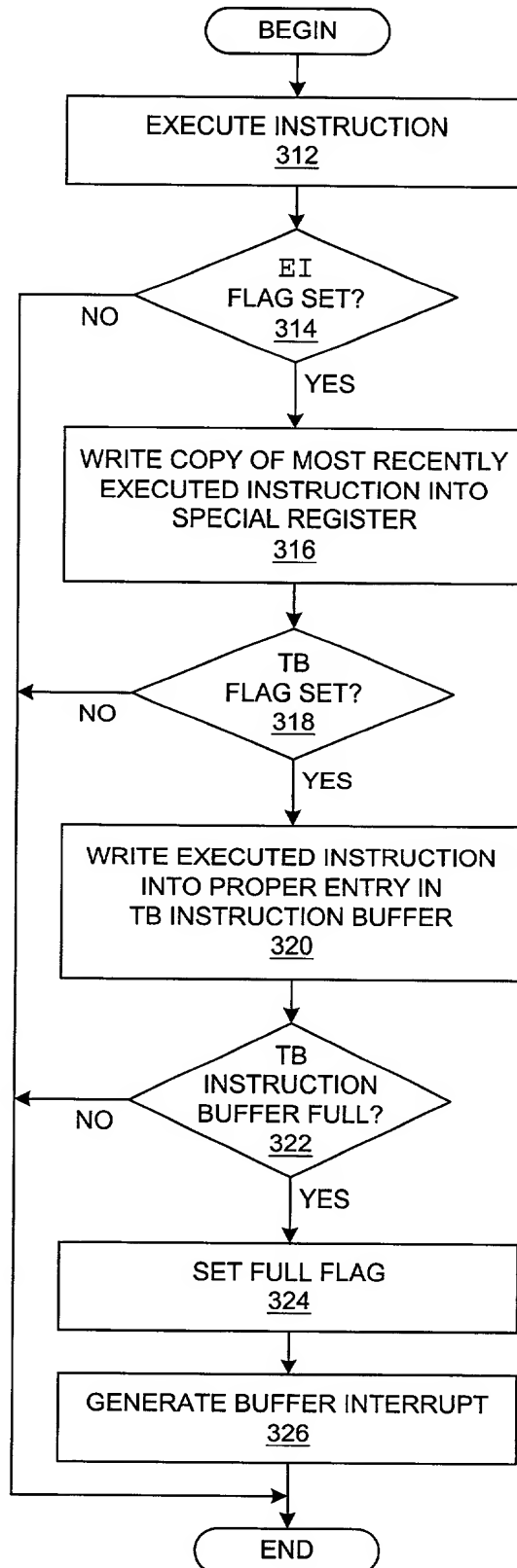


FIG. 3B

5/5

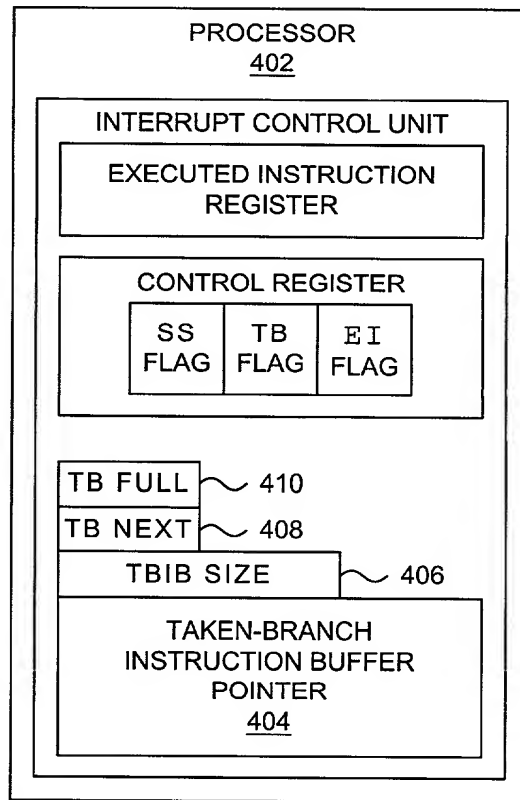


FIG. 4